

**Product Name:** ZX120T – PCISIG mini PCIe passive breakout extender adapter

**Product Description:** ZX120T is a mini PCIe passive breakout extender adapter module, providing access to all mini PCIe signals. ZX120T Mates with host. It is designed for real-time Test & Measurement, signal integrity, characterization, manufacturing test and debug of any mini PCIe design interface. ZX120T inner ground planes, stitching vias and the top & bottom GND fills are all connected together and accessible via the GND Test Points, TP, as well as the Mounting Holes, MH.

- 1- Provides access to ALL mini PCIe PCISIG signals via standard through hole, TH, test points.
- 2- ZX120T is designed to be inserted into the host for purpose of debugging, development, testing and characterization.
- 3- Listed number adjacent to each TH test point represents the associated mini PCIe connector's pin number.
- 4- All traces are 50 Ohms impedance controlled with exceptional signal integrity & crosstalk.
- 5- Four layers PCB design, inner layers are GND planes with direct connection to GND stitching vias & top/bottom GND fills + the mounting holes.
- 6- Mates with any standard mini PCIe connector.

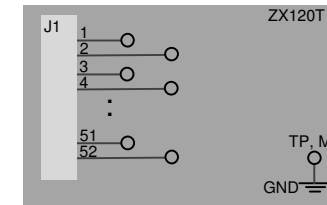
**Electrical:** Insertion loss > -2dB @3GHz  
 Trace impedance: 50 Ω  
 Operating Temperature: -65°C to +170°C  
 mini PCIe Edge Connector type ( J1 ) : mini PCIe, 52 position  
 Mates with: mini PCIe, 52 position  
 Plating: Gold 75U  
 Through Hole Test Point :  
 Diameter : Standard 0.1" ( 2.54mm ) post - 0.025" ( 0.064mm ) diameter  
 Plating: Gold 75U  
 Current per pin: 0.5A ( maximum)

**Application:** Bringup, testing, emulation, development, modular design evaluations, manufacturing loopback test, wifi GPS GYRO Compass BT FM sensor

**Mates with :** Any with any standard mini PCIe PCISIG host and device – TE 1775861 Molex SD-48338 and more

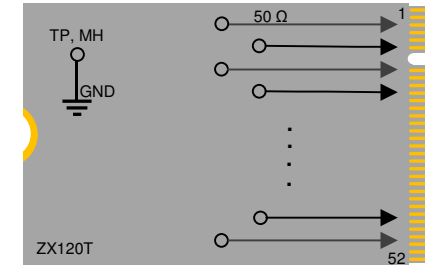
**Ground :** ZX120T inner ground planes, stitching vias and the top & bottom GND fills are all connected together and accessible via the GND Test Points, TP, as well as the Mounting Holes, MH.

Figure 1 Circuit diagram

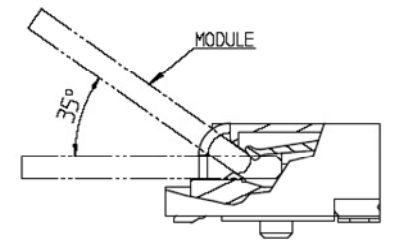
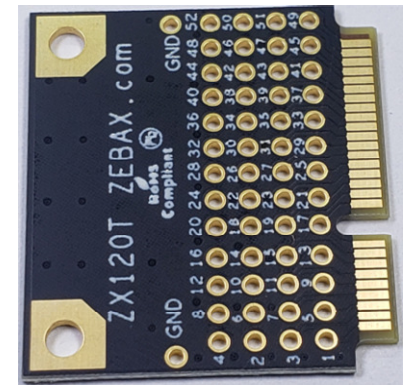
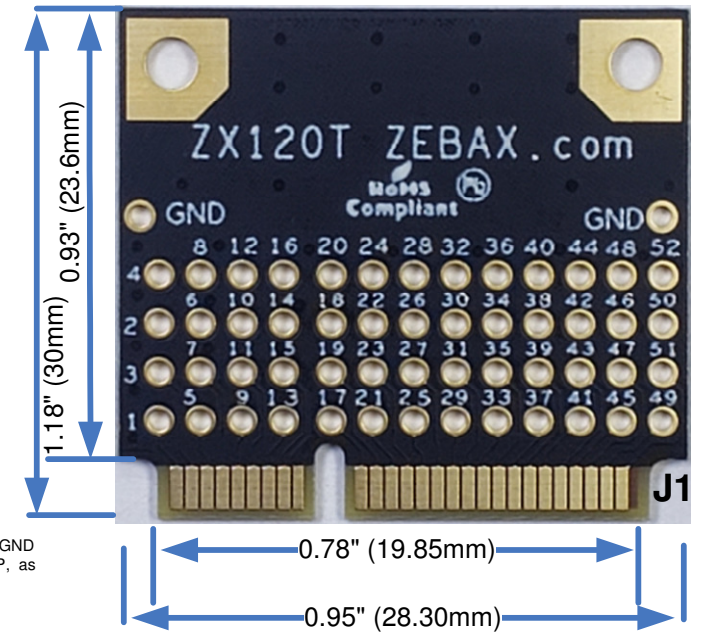


50 Ω : All traces are designed 50 Ω trace impedance control  
 J1 : mini PCIe PCISIG edge connector  
 GND – ZX120T inner ground planes, stitching vias and the top & bottom GND fills are all connected together and accessible via the GND test points, TP, as well as the Mounting Holes, MH.

Figure 2 – ZX120T Circuit diagram



50 Ω : All traces are designed 50 Ω trace impedance control  
 J2 : mini PCIe PCISIG receptacle connector  
 GND – ZX120T inner ground planes, stitching vias and the top & bottom GND fills are all connected together and accessible via the GND test points, TP, as well as the Mounting Holes, MH.



**Compliance:**

- ISO2001 certified
- RoHs - Lead Free
- EU RoHS2
- UL E111594 document
- ELV- Vehicle Directive ( Directive 2000/EC)
- European Union Directive ( 203/11/EC )
- Halogen Free per IEC-61249-2.21 : 2003
- RoHs Directive 2011/65/EU
- WEEE Directive ( 2012/12/EU)

- Certificate of Compliance for Radioactive substances
- Certificate of Compliance for Asbestos
- Certificate of Compliance for Ozone Depleting Substances, ODS
- Certificate REACH SVHC
- Certificate of Compliance RoHS\_EN\_CoC

**Ordering Information:**

Part number	Description
ZX120T	mini PCIe PCISIG passive breakout adapter

Note ALL ZEBAX products are RoHS compliant and Lead Free unless otherwise indicated.

<b>ZEBAX TECHNOLOGIES</b> SANTA CRUZ, CA U.S.A (831) 222-0717 WWW.ZEBAX.COM		
SPECIFIED DIMENSIONS ARE INCHES (MM). ROHS COMPLIANT	ASSEMBLY DRAWING	
	ITEM: ZX120T mini PCIe PCISIG	
DESCRIPTION: PCISIG mini PCIe passive breakout extender adapter		
CHECKED: M. MARINA	DRAWN: SONYA	REVISION: 1.0 SHEET: 1 OF 1

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