

NEWS LETTER

VITA 57.1 FPGA Mezzanine Card test modules

Zebax is introducing best in class Vita 57.1 FPGA mezzanine card test modules tailored for use with Agilent, or Tektronix logic analyzer using single or differential signal probes.

ZX182x is designed with matched trace impedance of 100Ω (differential trace impedance) using **12** layers PCB for improved signal integrity and performance. ZX182x is offered in High Pin Count, HPC, configuration where ALL the signals are accessible as followings:

Signals	Accessible
Power rails	2X5 pin header with LED indicator.
JTAG & I2C bus	2x5 pin header
All Clocks	Onboard IPEX37 connector

All VITA 57.1 signals are accessible via logic analyzer probe including Mictor (E5346A P6434), Samtec (E5379A), Touchless (P6880), and D-MAX (P6982).

Modules Description

ZX180	Provides all 10x40 = 400 signals via 0402 SMD package.
ZX182P	Designed for P6880 (P68xx Tektronix TLA7Axx series Logic Analyzer)
ZX182S	Supports Agilent logic analyzers using E5379A Samtec probe.
ZX182D	Supports P6892 Tektronix D-Max series probes
ZX182M	Offers both Agilent and Tektronix legacy Mictor probes.

Each module is fully assembled with IPEX, headers, the power LED indicators, and the Vita 57.1 Samtec MC and CC connectors for all the FPGA Mezzanine cards. Additionally Mictor, and Samtec connectors for ZX182M and ZX182S modules are assembled for the respective modules.

Features:

- 1- Access to **JTAG, and I2C bus** via 2x5 pin (2mm pitch) headers.
- 2- Access and **all power rails** (via 2x5 pin, 2mm pitch, headers) with onboard **LED** indicators .
- 3- All **GBTCLK0_M2C GBTCLK1_M2C CLK0_M2C CLK1_M2C, CLK2_BIDIR CLK3_BIRID** differential clock signals are accessible via onboard **IPEX-37** connectors; enabling user to analyze, induce or characterize clocks for jitter, slew, and more..
- 4- Available multiple probe clock triggering options via 0402 SMD 0 Ohms stuffing resistor. Please see datasheet for details.
- 5- ALL of the Vita 57.1 signals are available at the designated logic analyzer probe connectors.

Easy scope interface using Zebax breakout adapters in addition to the designated Logic Analyzer probes [ZX104x](#) (Mictor breakout adapter) or [ZX132x](#) for using the ZX182M or ZX182S Vita 57.1 FPGA mezzanine card test modules respectively.

