

Product Name: ZX180V-HPC FMC Vita 57.1 test module – accessing all Vita 57.1 HPC signals

Product Description: FPGA Mezzanine card , FMC , passive test module meeting VITA 57.1 standard bus interface. Includes 10 rows x 40 pins, totaling **400** pins, High Pin Count , HPC, housing both SEAM and SEAF connectors. Fully compatible with Low Pin Count, LPC connector interface.

Full access to all (excluding the GND signals) Vita 57.1 HPC signals via onboard 0402 SMD footprint. Please see **Page 2** for full list of accessible signals as listed by Vita 57.1 standard. The Vita57.1 assigned GND signals are not accessible individually, they are connected to inner GND planes as well as top/bottom layers fill. The GND access point is offered by 2 onboard GND test points interfacing with test equipment, host and target.

- Mates with Samtec Molex HI-SPEED HI-DENSITY SEARRAY design connectors.
- **Fully** compatible with 10 rows x 40 pins per row single ended or differential pairs design configuration
- Designed in **8** layer PCB stackup
- **All** signals are accessible via onboard standard 0402 SMD footprint.
- All signals (via 0402 SMD package) are pass through, enabling user to implement design changes (cut signal path), if design changes are required.
- Improved signal integrity and crosstalk
- Multiple GND test points connecting directly to inner layers GND planes as well as ALL Vita 57.1 GND signals.
- Includes both HPC MC (SEAM) and CC (SEAF) connectors
- Matching connector's **50Ω** trace impedance on all signals – Reference plane impedance 50Ω for DC to 10GHz bandwidth applications

Application: FMC VITA 57.1 daughter card Bringup, testing, emulation, Xilinx development Virtex 6 Virtex 7 interface testing daughter board to host, modular design evaluations

Access: All signals (excluding the GND signals of Vita 57.1 standard) are accessible via onboard 0402 SMD footprint.

Pitch: 1.27mm (0.05") High Speed connector

Mates with : Xilinx FPGA development systems Virtex 6 Virtex 7 connecting daughter board to Host
Any and all FMC VITA 57.1 compliant design.
SEAM SADL SEAMP SEAR SEAMI SEAC FMC HPC LCP
SEAF-040-08.0-L-10-2-A SEAF-040-08-L-10-2-A
SEAFP-40 SEAMP-040 SEAMI-040 SEAR-040-10-10- SEAM-040
All listed Samtec Molex FMC connectors listed, table below:

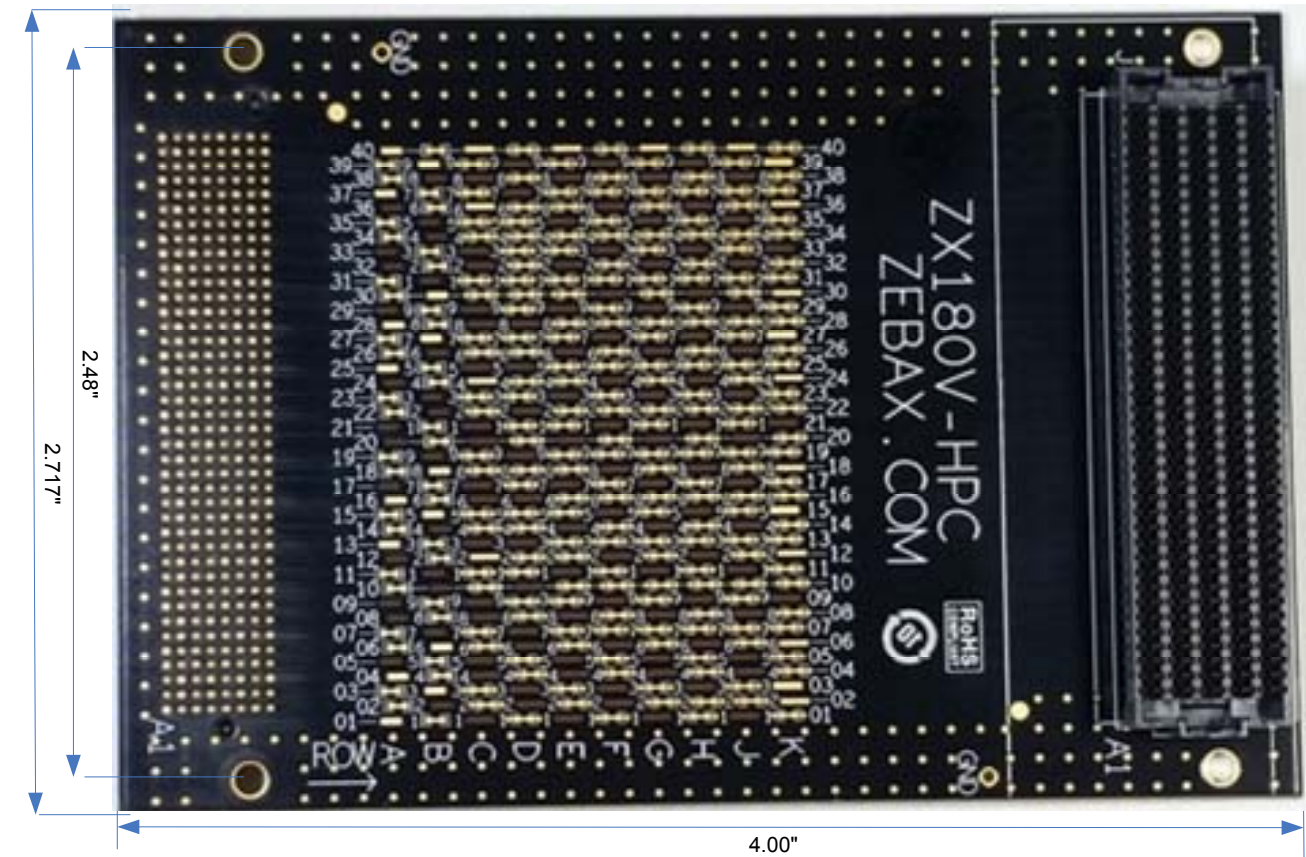
ZX180V-HPC FMC breakout adapter mates with the following Samtec Molex CC / MC SEARRAY™ VITA 57.1 Connectors

Molex PN	Samtec PN	VITA PN	Description	Mated Stack Height
45971-4307	ASP-127796-01*	CC-LPC-10L	female	
45971-4317	ASP-134485-01*	CC-HPC-10L	female	
45971-4315	ASP-134486-01	CC-HPC-10	female	
45971-4305	ASP-134603-01	CC-LPC-10	female	
45970-4117	ASP-134601-01*	MC-HPC-8.5L	male	8.5 mm
45970-4115	ASP-134602-01	MC-HPC-8.5	male	8.5 mm
45970-4107	ASP-134605-01*	MC-LPC-8.5L	male	8.5 mm
45970-4105	ASP-134606-01	MC-LPC-8.5	male	8.5 mm
45970-4307	ASP-127797-01	MC-LPC-10L	male	10 mm
45970-4317	ASP-134487-01	MC-HPC-10L	male	10 mm
45970-4315	ASP-134488-01	MC-HPC-10	male	10 mm
45970-4305	ASP-134604-01	MC-LPC-10	male	10 mm

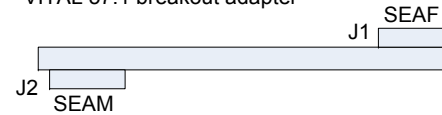
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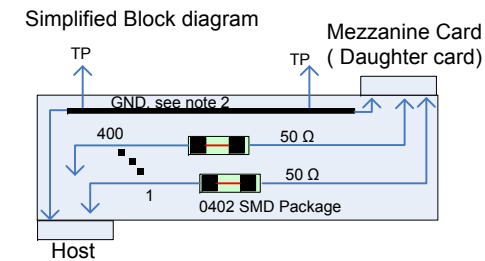
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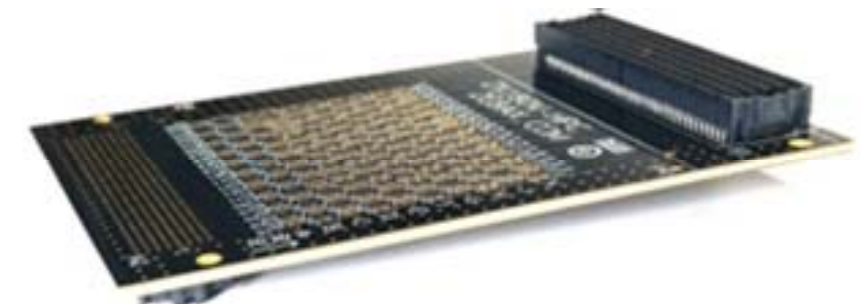
ZX180V-HPC , Passive FMC VITAL 57.1 breakout adapter



- Note:**
1- SEAM connector type interfaces with HOST.
2- SEAF connector type interfaces with Mezzanine Card.



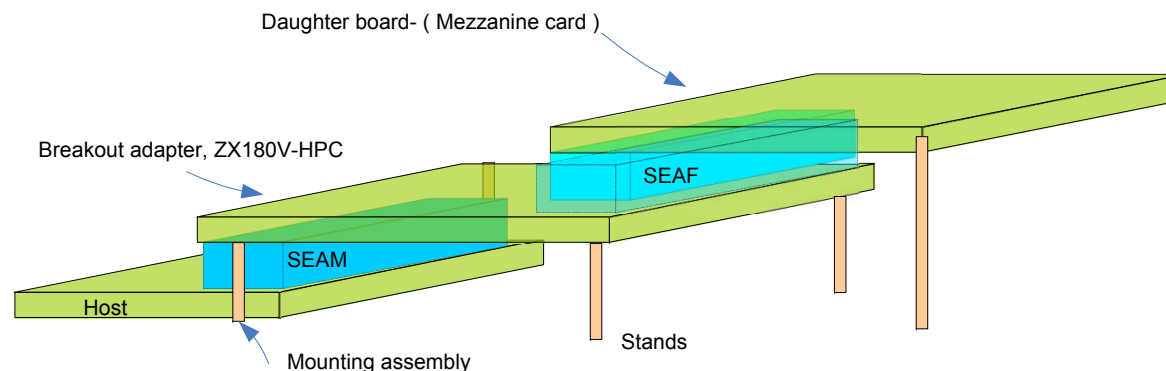
- Note:**
1- All Vita 57.1 signals are accessible.
2- All Vita 57.1 reserved GND signals are accessible via GND Test points.



Typical signal connection: 0402 SMD Package

Break signal path:

Note
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SPECIFIED DIMENSIONS ARE INCHES (MM). ROHS COMPLIANT

ASSEMBLY DRAWING

ITEM: ZX180V-HPC

DESCRIPTION: FMC VITA 57.1 breakout adapter – test module FPGA mezzanine card HPC

CHECKED: M. MARINA

DRAWN: SLAVIK

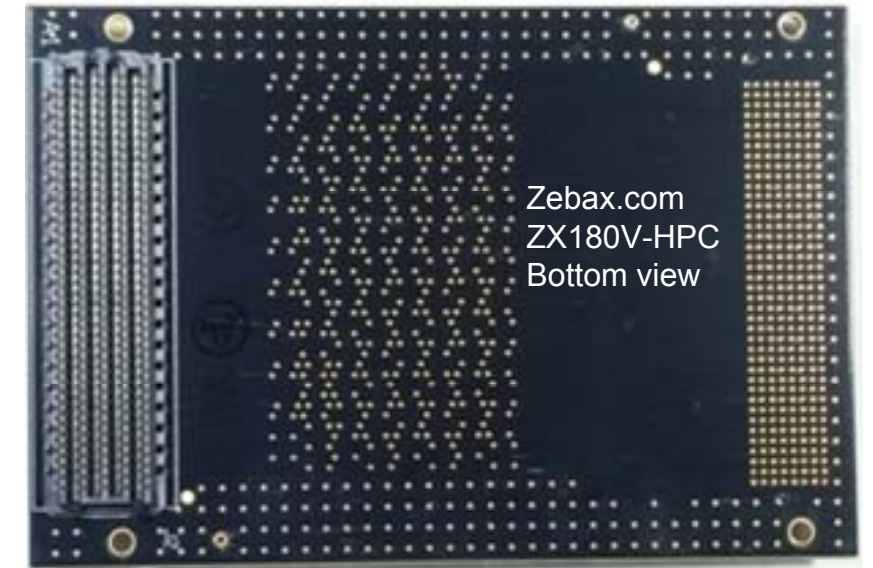
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SHEET: 1 OF 2

Product Name: Cont's ZX180V-HPC FMC Vita 57.1 test module – accessing all Vita 57.1 HPC signals

Ground: ZX180V-HPC is breakout adapter – test module , offering VITA 57.1 signals. It enables user to implement design changes (cut signal path) , or simply access the Vita 57.1 signals for test and measurement purpose.
The Vita 57.1 GND reserved signals on the ZX180V-HPC are connected to the module inner GND planes and top & bottom GND fills. The GND access point is offered by 2 onboard GND test points interfacing with test equipment, host and target.

Access signals: ZX180V-HPC provides access to all Vita 57.1 signals (excluding the GND signals) via onboard 0402 SMD footprint package. Table below lists the Vita 57.1 signals , to be used as reference accessing ZX180V-HPC FMC Vita 57.1 test module breakout adapter.



Zebax.com
ZX180V-HPC
Bottom view

Pin	K	J	H	G	F	E	D	C	B	A
	Signal	Signal	Signal	Signal	Signal	Signal	Signal	Signal	Signal	Signal
1	VREF_B_M2C	GND	VREF_A_M2C	GND	PG_M2C	GND	PG_C2M	GND	CLK_DIR	GND
2	GND	CLK3_BIDIR_P	PRSNT_M2C_L	CLK1_M2C_P	GND	HA01_P_CC	GND	DP0_C2M_P	GND	DP1_M2C_P
3	GND	CLK3_BIDIR_N	GND	CLK1_M2C_N	GND	HA01_N_CC	GND	DP0_C2M_N	GND	DP1_M2C_N
4	CLK2_BIDIR_P	GND	CLK0_M2C_P	GND	HA00_P_CC	GND	GBTCLK0_M2C_P	GND	DP9_M2C_P	GND
5	CLK2_BIDIR_N	GND	CLK0_M2C_N	GND	HA00_N_CC	GND	GBTCLK0_M2C_N	GND	DP9_M2C_N	GND
6	GND	HA03_P	GND	LA00_P_CC	GND	HA05_P	GND	DP0_M2C_P	GND	DP2_M2C_P
7	HA02_P	HA03_N	LA02_P	LA00_N_CC	HA04_P	HA05_N	GND	DP0_M2C_N	GND	DP2_M2C_N
8	HA02_N	GND	LA02_N	GND	HA04_N	GND	LA01_P_CC	GND	DP8_M2C_P	GND
9	GND	HA07_P	GND	LA03_P	GND	HA09_P	GND	LA01_N_CC	GND	DP8_M2C_N
10	HA06_P	HA07_N	LA04_P	LA03_N	HA08_P	HA09_N	GND	LA06_P	GND	DP3_M2C_P
11	HA06_N	GND	LA04_N	GND	HA08_N	GND	LA05_P	LA06_N	GND	DP3_M2C_N
12	GND	HA11_P	GND	LA08_P	GND	HA13_P	LA05_N	GND	DP7_M2C_P	GND
13	HA10_P	HA11_N	LA07_P	LA08_N	HA12_P	HA13_N	GND	LA09_P	GND	DP7_M2C_N
14	HA10_N	GND	LA07_N	GND	HA12_N	GND	LA09_P	LA10_P	GND	DP4_M2C_P
15	GND	HA14_P	GND	LA12_P	GND	HA16_P	LA09_N	LA10_N	GND	DP4_M2C_N
16	HA17-P-CC	HA14_N	LA11_P	LA12_N	HA15_P	HA16_N	GND	LA14_N	GND	DP6_M2C_P
17	HA17-N-CC	GND	LA11_N	GND	HA15_N	GND	LA13_P	GND	DP6_M2C_N	GND
18	GND	HA18_P	GND	LA16_P	GND	HA20_P	LA13_N	LA14_P	GND	DP5_M2C_P
19	HA21_P	HA18_N	LA15_P	LA16_N	HA19_P	HA20_N	GND	LA14_N	GND	DP5_M2C_N
20	HA21_N	GND	LA15_N	GND	HA19_N	GND	LA17_P_CC	GND	GBTCLK1_M2C_P	GND
21	GND	HA22_P	GND	LA20_P	GND	HB03_P	LA17_N_CC	GND	GBTCLK1_M2C_N	GND
22	HA23_P	HA22_N	LA19_P	LA20_N	HB02_P	HB03_N	GND	LA18_P_CC	GND	DP1_C2M_P
23	HA23_N	GND	LA19_N	GND	HB02_N	GND	LA23_P	LA18_N_CC	GND	DP1_C2M_N
24	GND	HB01_P	GND	LA22_P	GND	HB05_P	LA23_N	GND	DP9_C2M_P	GND
25	HB00-P-CC	HB01_N	LA21_P	LA22_N	HB04_P	HB05_N	GND	GND	DP9_C2M_N	GND
26	HB00-N-CC	GND	LA21_N	GND	HB04_N	GND	LA26_P	LA27_P	GND	DP2_C2M_P
27	GND	HB07_P	GND	LA25_P	GND	HB09_P	LA26_N	LA27_N	GND	DP2_C2M_N
28	HB06-P-CC	HB07_N	LA24_P	LA25_N	HB08_P	HB09_N	GND	GND	DP8_C2M_P	GND
29	HB06-N-CC	GND	LA24_N	GND	HB08_N	GND	TCK	GND	DP8_C2M_N	GND
30	GND	HB11_P	GND	LA29_P	GND	HB13_P	GND	SCL	GND	DP3_C2M_P
31	HB10-P	HB11_N	LA28_P	LA29_N	HB12_P	HB13_N	TDO	GND	GND	DP3_C2M_N
32	HB10-N	GND	LA28_N	GND	HB12_N	GND	3P3VAUX	GND	DP7_C2M_P	GND
33	GND	HB15_P	GND	LA31_P	GND	HB19_P	TMS	GND	DP7_C2M_N	GND
34	HB14-P	HB15_N	LA30_P	LA31_N	HB16_P	HB19_N	TRST_L	GA0	GND	DP4_C2M_P
35	HB14-N	GND	LA30_N	GND	HB16_N	GND	GA1	12P0V	GND	DP4_C2M_N
36	GND	HB18_P	GND	LA33_P	GND	HB21_P	3P3V	GND	DP6_C2M_P	GND
37	HB17_P_CC	HB18_N	LA32_P	LA33_N	HB20_P	HB21_N	GND	12P0V	DP6_C2M_N	GND
38	HB17_N_CC	GND	LA32_N	GND	HB20_N	GND	3P3V	GND	GND	DP5_C2M_P
39	GND	VIO_B_M2C	GND	VADJ	GND	VADJ	GND	3P3V	GND	DP5_C2M_N
40	VIO_B_M2C	GND	VADJ	GND	VADJ	GND	3P3V	GND	RES0	GND

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